

Mini8118

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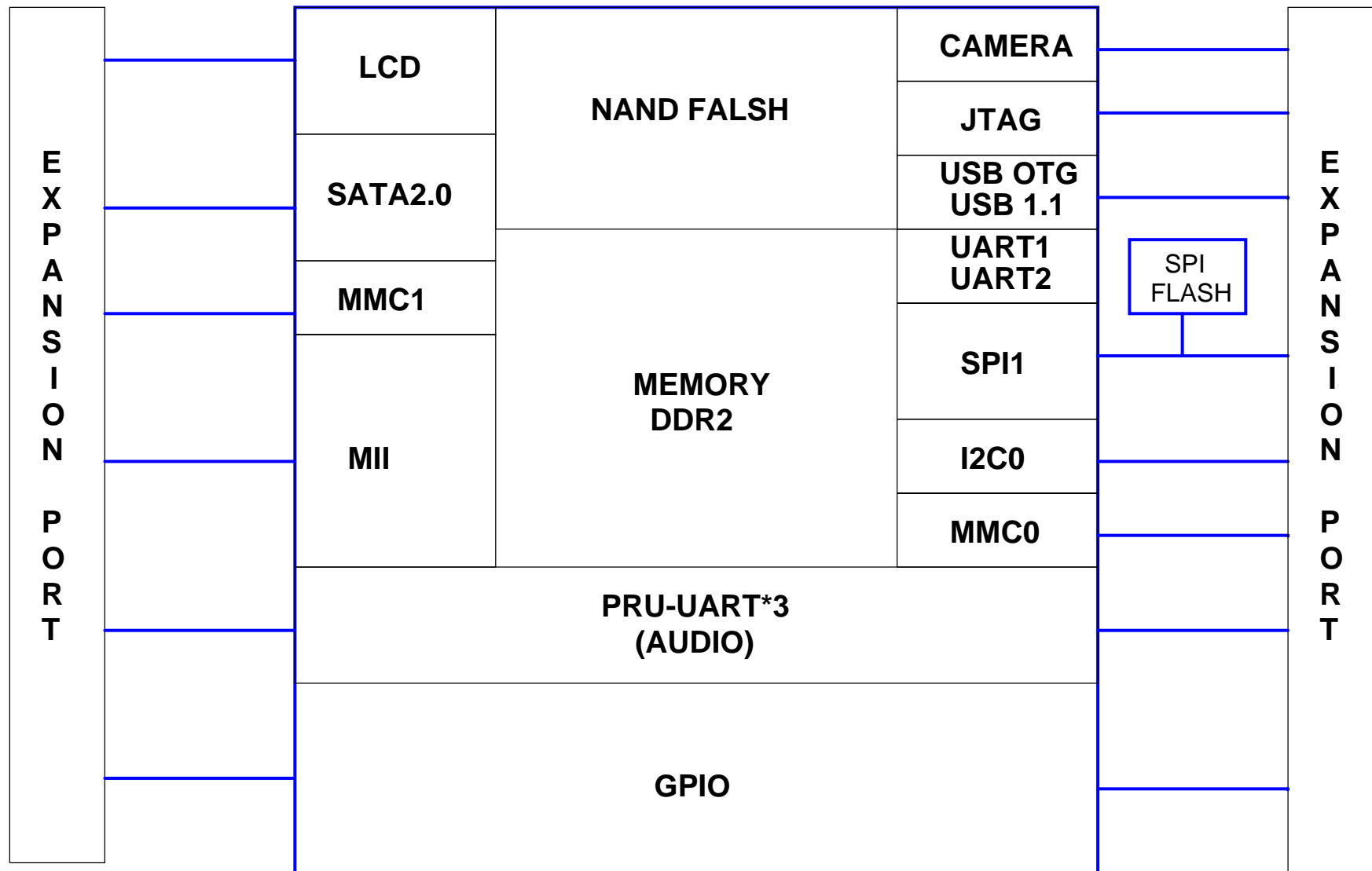
Schematic History

2012-05-10

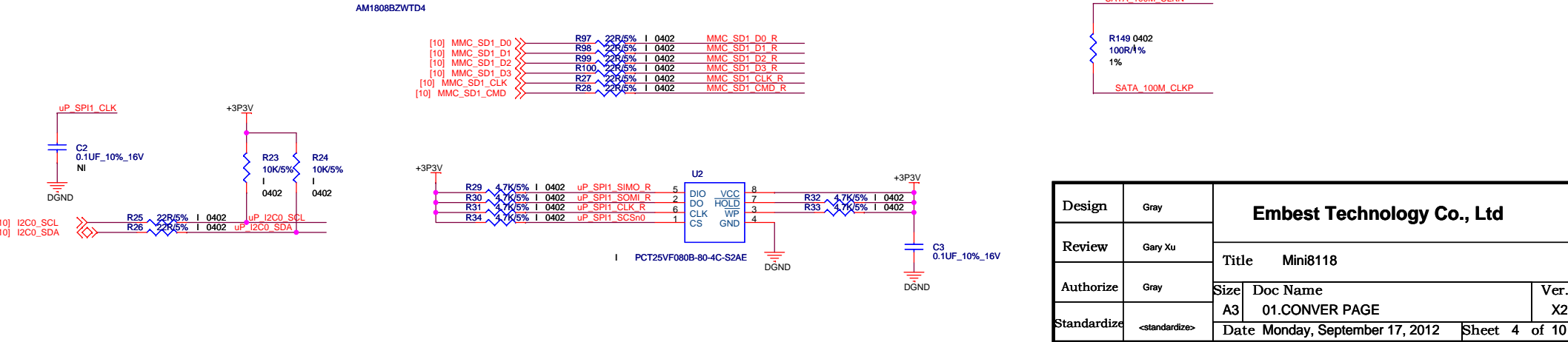
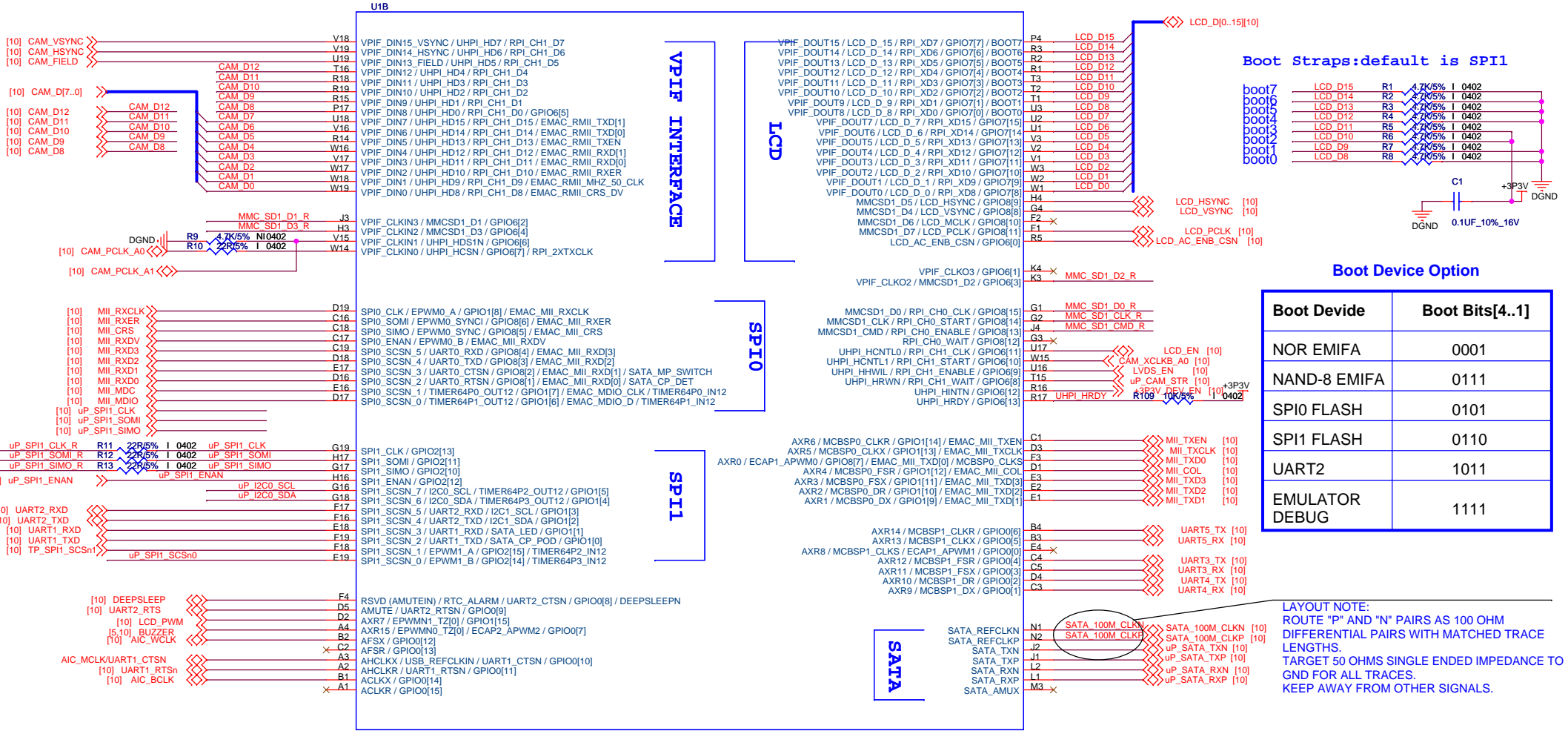
- 1.Change Reset Function
- 2.Remove reserve Function

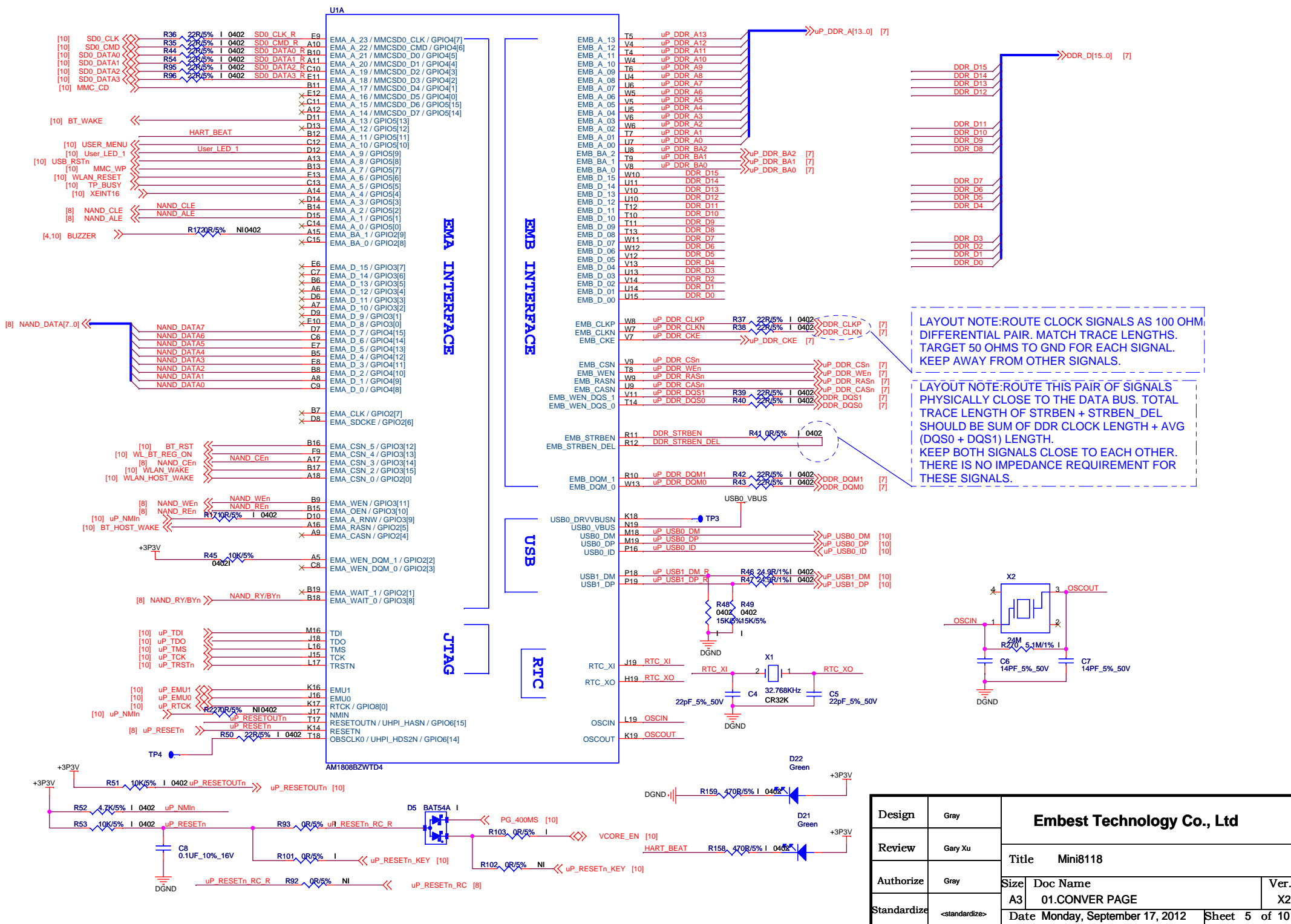
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Mini8118 Core-board



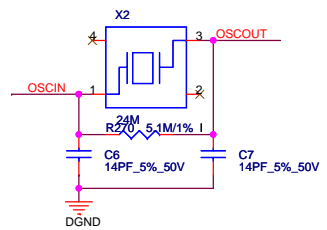
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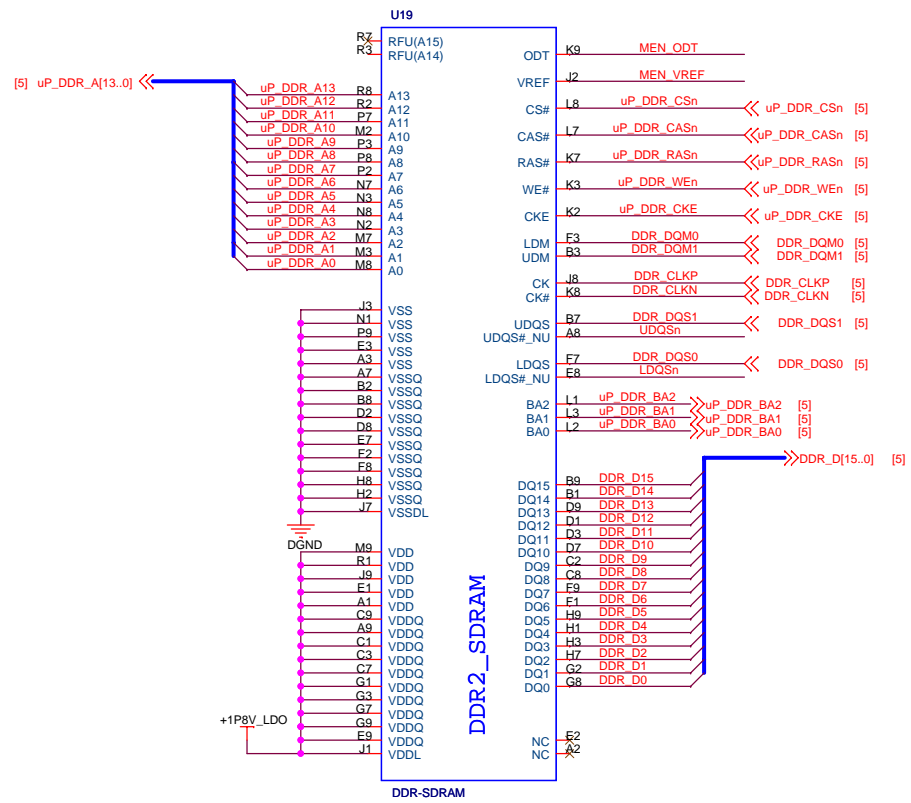
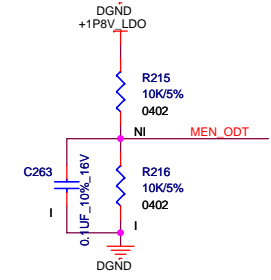
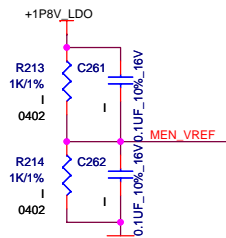
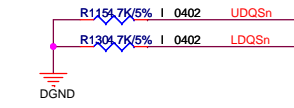
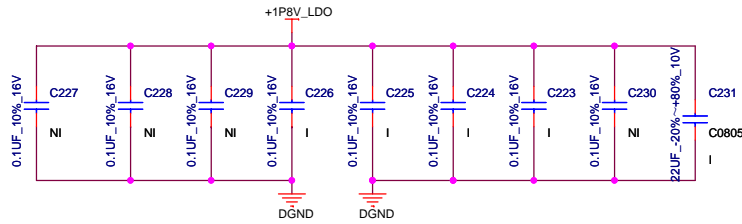


LAYOUT NOTE: ROUTE CLOCK SIGNALS AS 100 OHM DIFFERENTIAL PAIR. MATCH TRACE LENGTHS. TARGET 50 OHMS TO GND FOR EACH SIGNAL. KEEP AWAY FROM OTHER SIGNALS.

LAYOUT NOTE: ROUTE THIS PAIR OF SIGNALS PHYSICALLY CLOSE TO THE DATA BUS. TOTAL TRACE LENGTH OF STRBEN + STRBEN_DEL SHOULD BE SUM OF DDR CLOCK LENGTH + AVG (DQS0 + DQS1) LENGTH. KEEP BOTH SIGNALS CLOSE TO EACH OTHER. THERE IS NO IMPEDANCE REQUIREMENT FOR THESE SIGNALS.

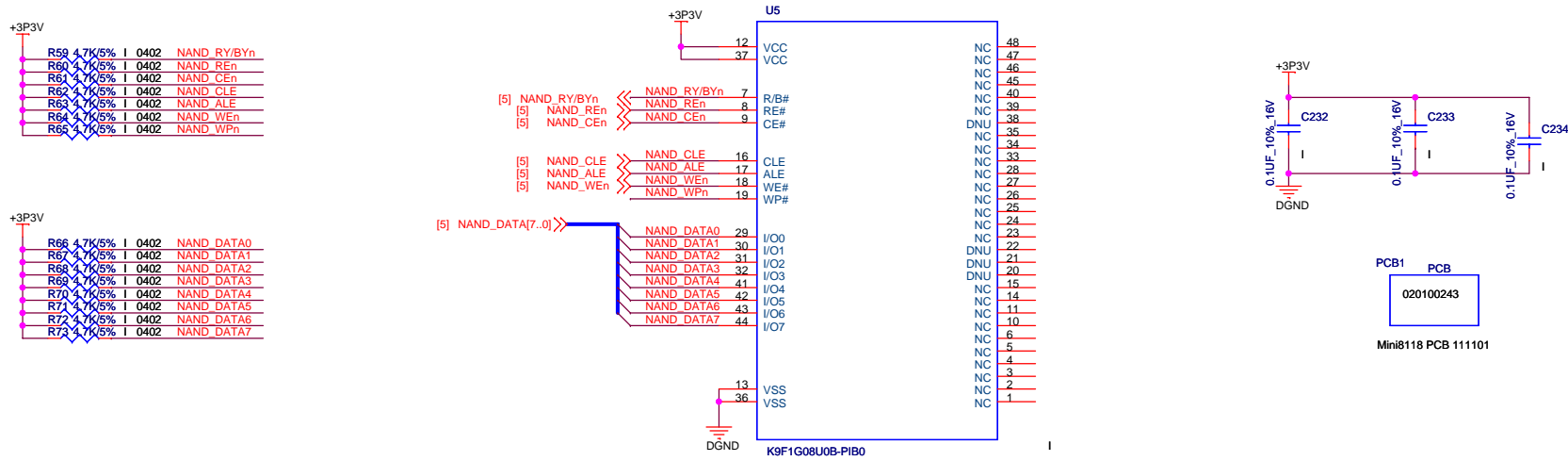


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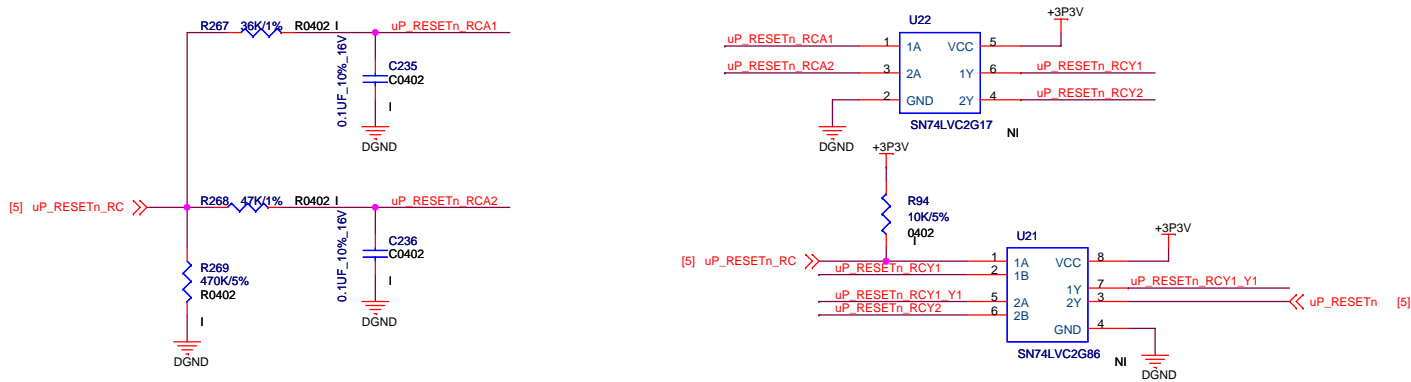


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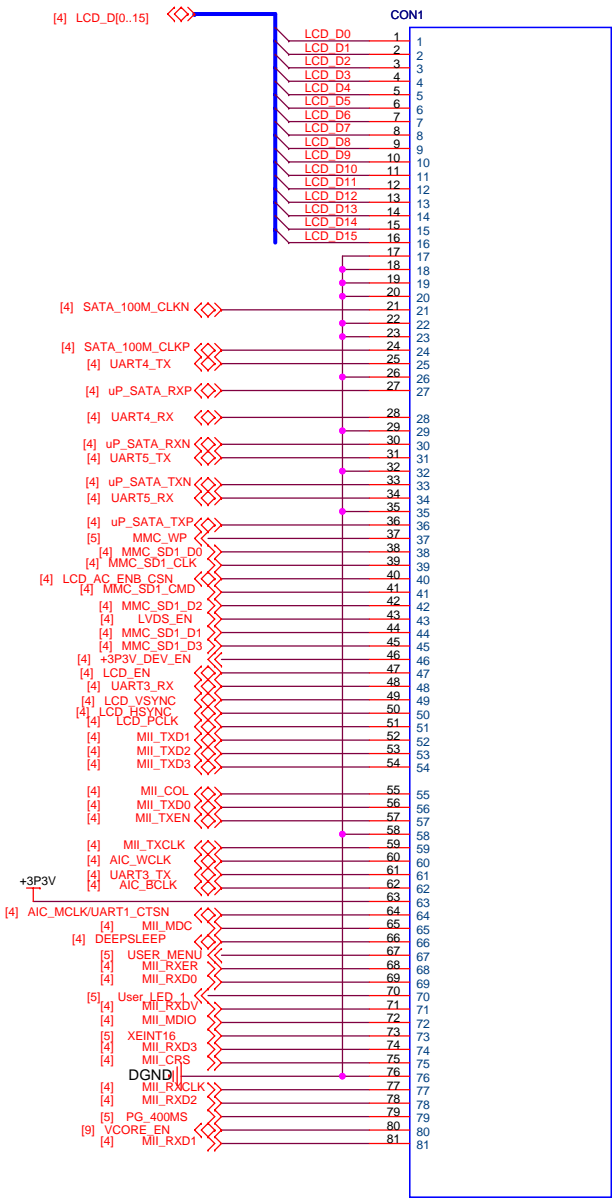
NAND FLASH



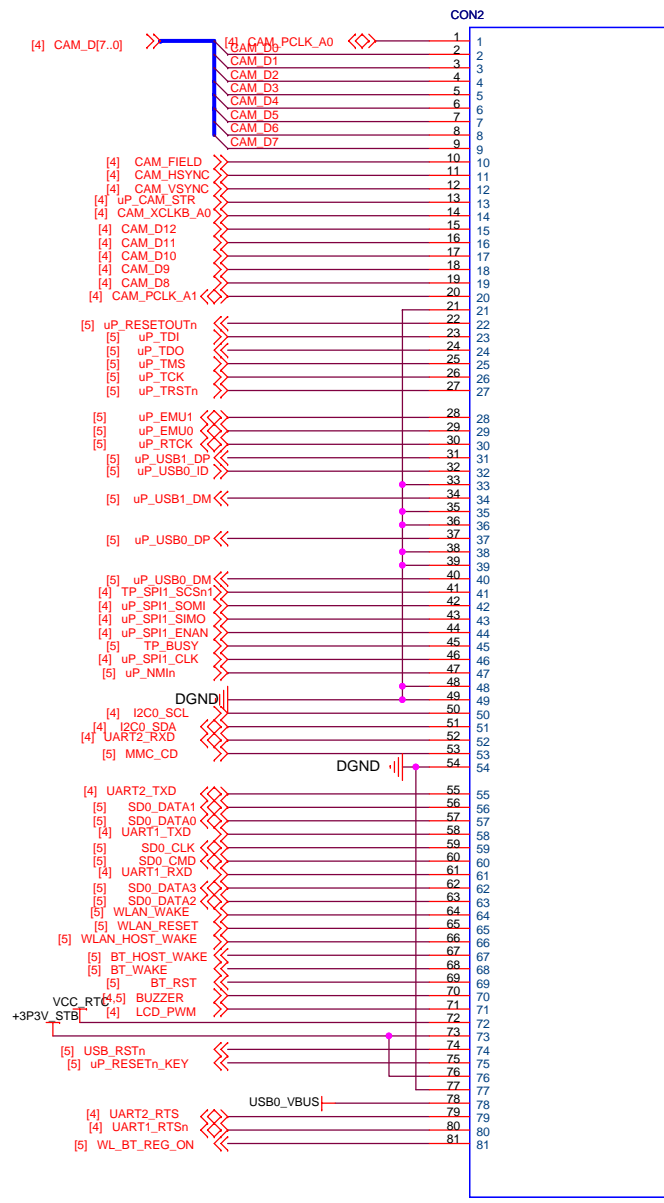
RESERVE



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Con_3R_81P



Con_3R_81P

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