

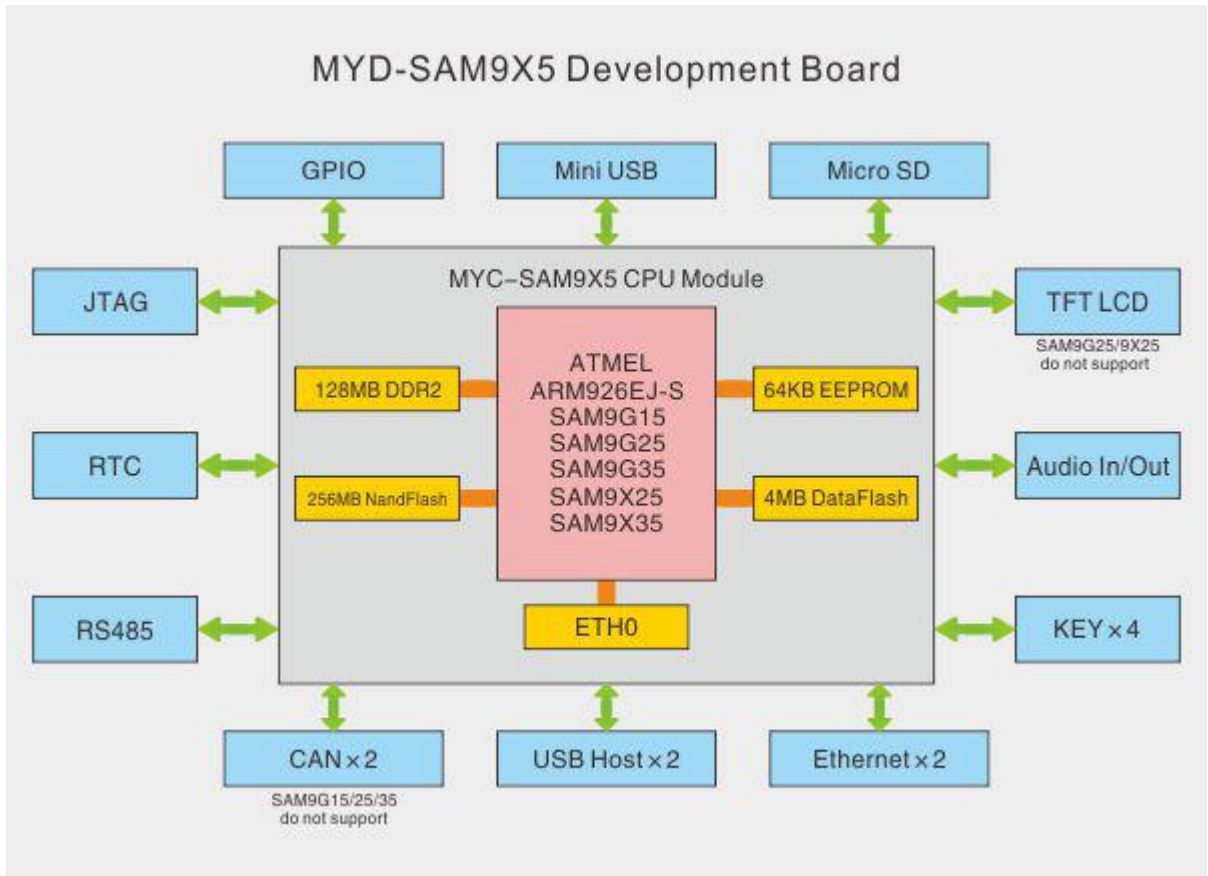
Hardware Specification

The Atmel® SAM9G and SAM9X embedded MPUs are high-performance, highly integrated processors built the good foundation of the Atmel® ARM926-based embedded MPU line. Running at 400 MHz, they are designed to complement the power of the ARM926 core; these flexible devices deliver a rich combination of peripherals including up to two Ethernet, two CAN, three USB ports and seven UARTS. Additional features include an integrated soft modem, TFT LCD controller and LPDDR/DDR2 memory support. A multilayer bus matrix architecture and multiple DMA channels ensure uninterrupted data transfer with minimum processor intervention. Low voltage, low power consumption and reduced system cost make these devices ideal for cost-sensitive machine-to-machine applications.

Item	MYC-SAM9G15	MYC-SAM9G25	MYC-SAM9G35	MYC-SAM9X25	MYC-SAM9X35
	AT91SAM9G15	AT91SAM9G25	AT91SAM9G35	AT91SAM9X25	AT91SAM9X35
Processor	<ul style="list-style-type: none"> - 400 MHz ARM926EJ-S Processor - DSP Instruction Extensions - ARM Jazelle Technology - 16 KB Data Cache, 16 KB Instruction Cache with MMU - 32KB Internal SRAM - External Bus Interface - DMA Controller - DDR2 Interface - IIS audio interface - 217 balls BGA - Low power, Vcore 1.0V 				
Dimensions	67.6 x 35mm				
PCB Layer	8-layer design				
Power Supply	3.3V				
Working Temp.	0~70 Celsius or -40~85 Celsius				
External Memory	128MB DDR2 SDRAM, 256MB Nand Flash, 4MB Data Flash, 64KB EEPROM				

RTC	32.768kHz crystal for RTC clock				
LED	One Power Indicator (Red) One User LED (Blue)				
Expansion Interface	1.8V DDR2 SO-DIMM 200-pin connector brings out below peripheral signals: <i>Note: The resources brought out from the expansion interfaces may be multiplexed with others.</i>				
Ethernet	0	1	1	2	1
CAN	0	0	0	2	2
LCD	1	0	1	0	1
Serial Ports	6	7	6	7	6
MMC/SD	2				
USB	3				
ADC	12-channel 10-bit ADC				
SPI	2				
TWI	2				
PWM	4 x 16-bit PWM				
GPIOs	Up to 105 GPIOs can be used				

MYD-SAM9X5 Development Board Block Diagram



Dimension Chart of MYC-SAM9X5

