MINI8600B 120900

Design	MITCH	Embest Tech Co. LTD	
Review	<review>	Title00 INDEX PAGE	
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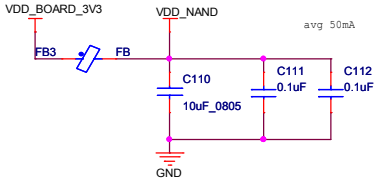
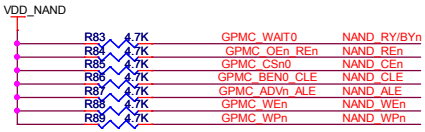
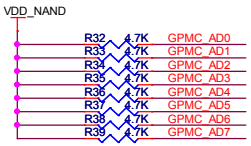
Standardize	<standardize>	Date: Friday, September 28, 2012	Sheet 1 of 11







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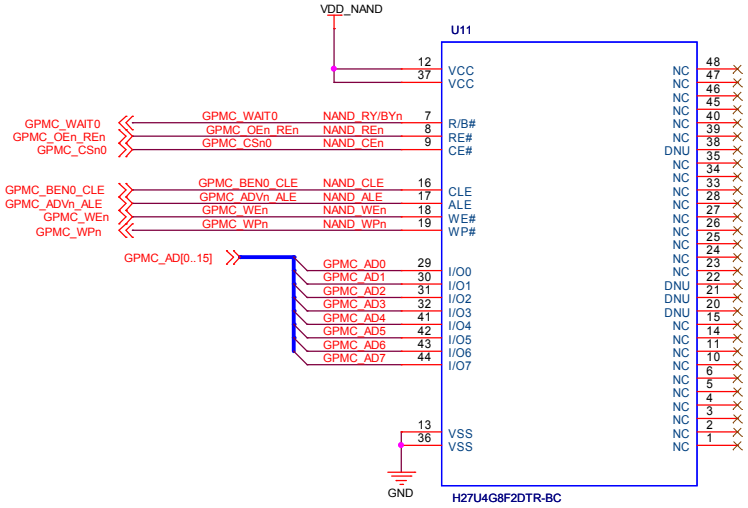


Design	MITCH	**Embest Tech Co. LTD**	
Review	<review>	Title 06 NAND	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 1 of 12

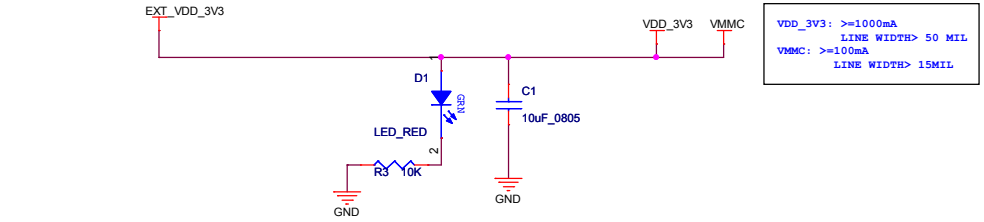
Design	MITCH	**Embest Tech Co. LTD**	
Review	<review>	Title 02 BLOCK	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 2 of 11

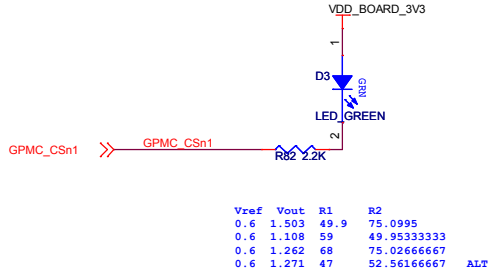
HARDWARE INSTRUCTION

PCB LAYOUT GUIDLE

Design	MITCH	Embest Tech Co. LTD	
Review	<review>	Title 03 INSTRUCTION	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 / Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 3 of 11

VDD_3V3: 3.3V, 5%,650mA MIN





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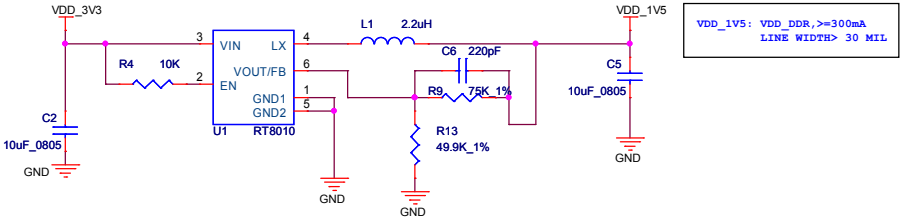
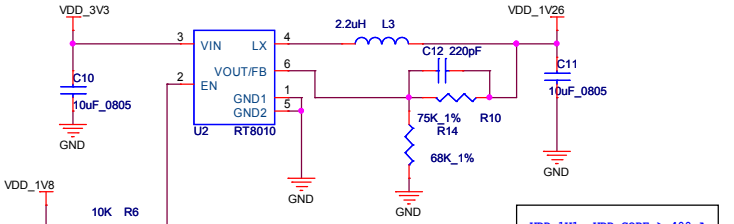
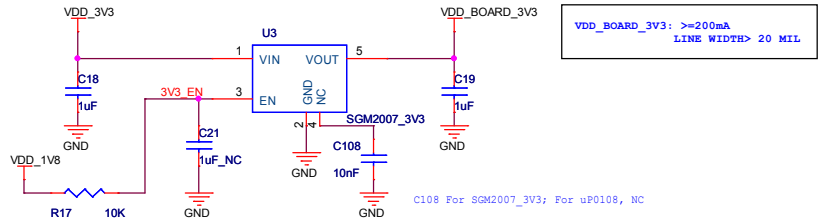
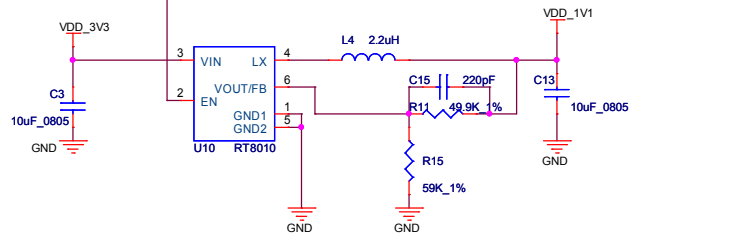
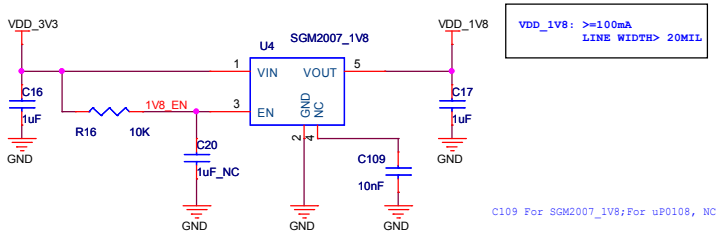
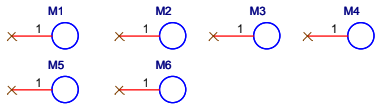


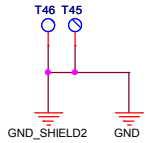


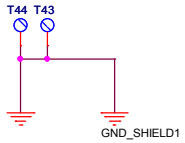




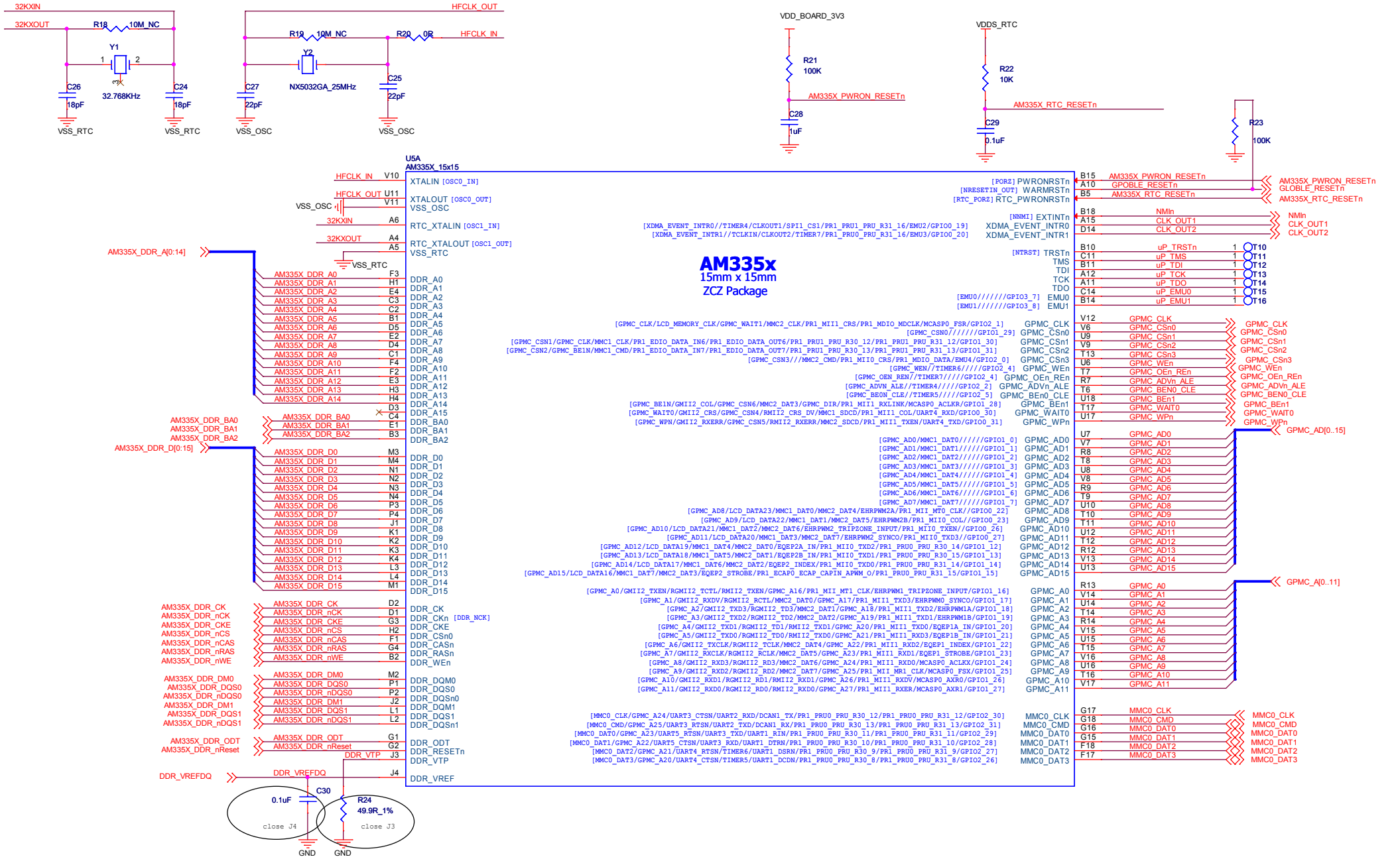




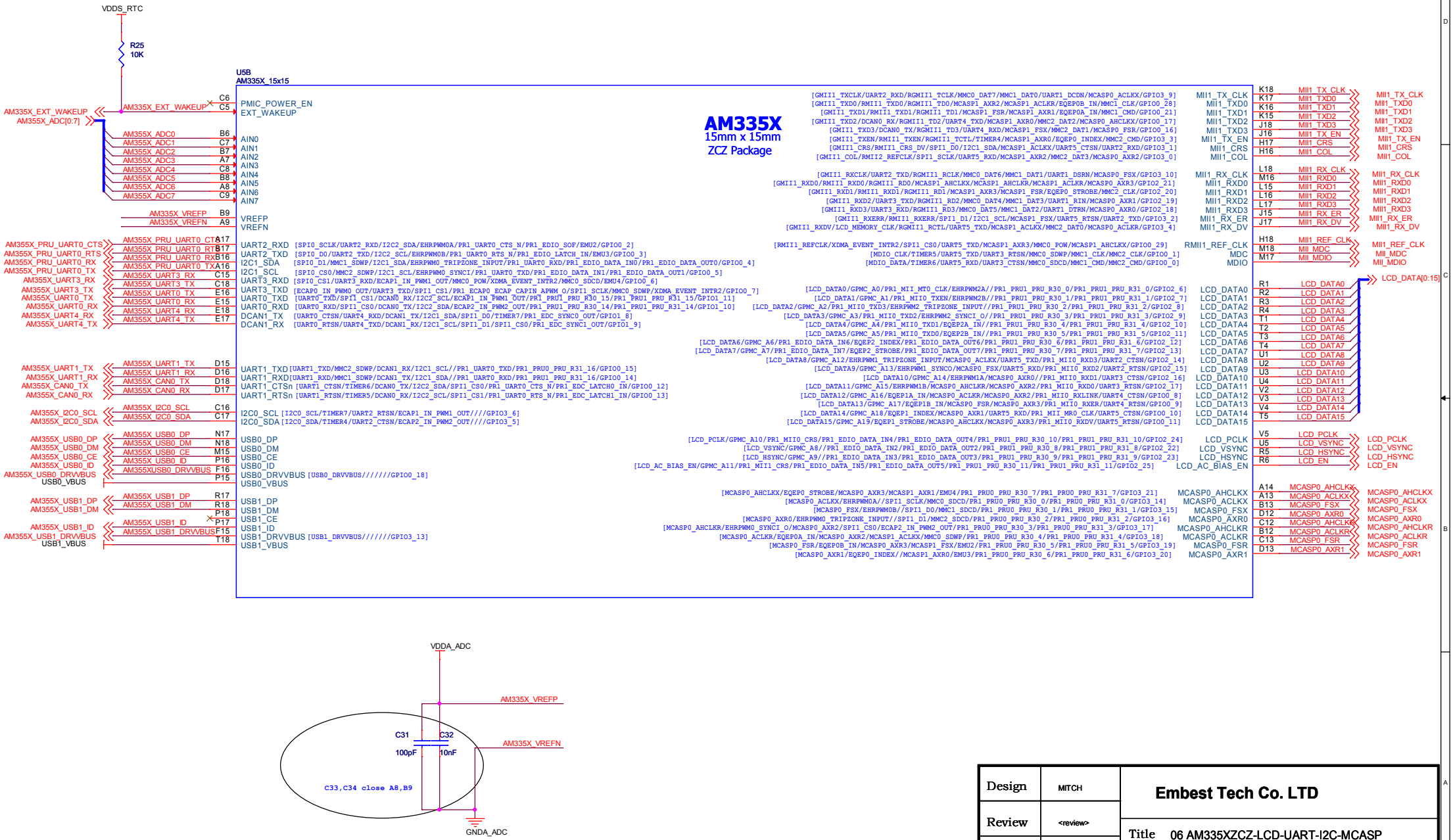




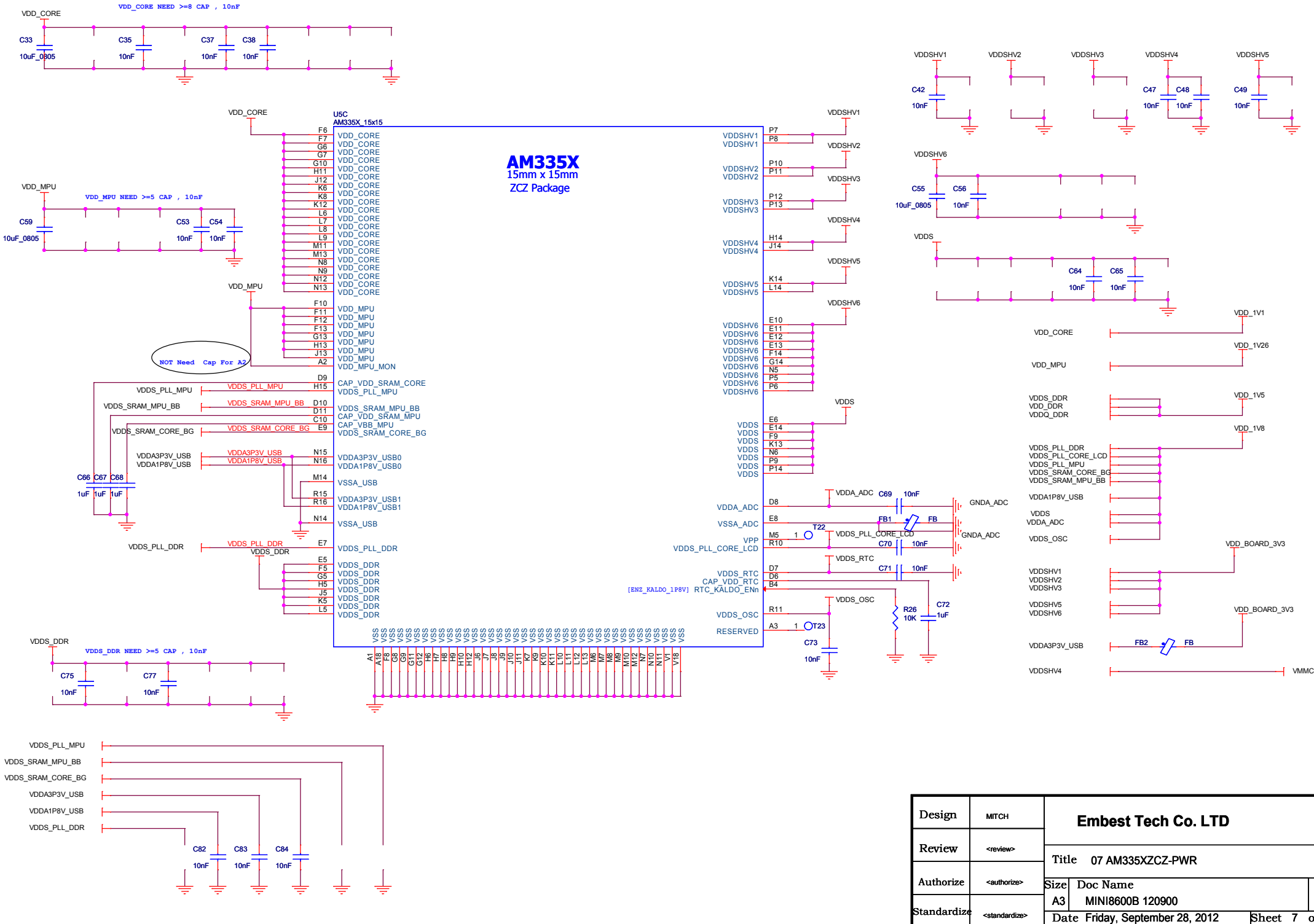
Design	MITCH	Embest Tech Co. LTD	
Review	<review>	Title 01 PWR	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 4 of 11



Design	MITCH	**Embest Tech Co. LTD**	
Review	<review>	Title 02 AM335XZCZ-DDR-NAND-IF	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 5 of 11



Design	MITCH	**Embest Tech Co. LTD**	
Review	<review>	Title 06 AM335XZCZ-LCD-UART-I2C-MCASP	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 6 of 11



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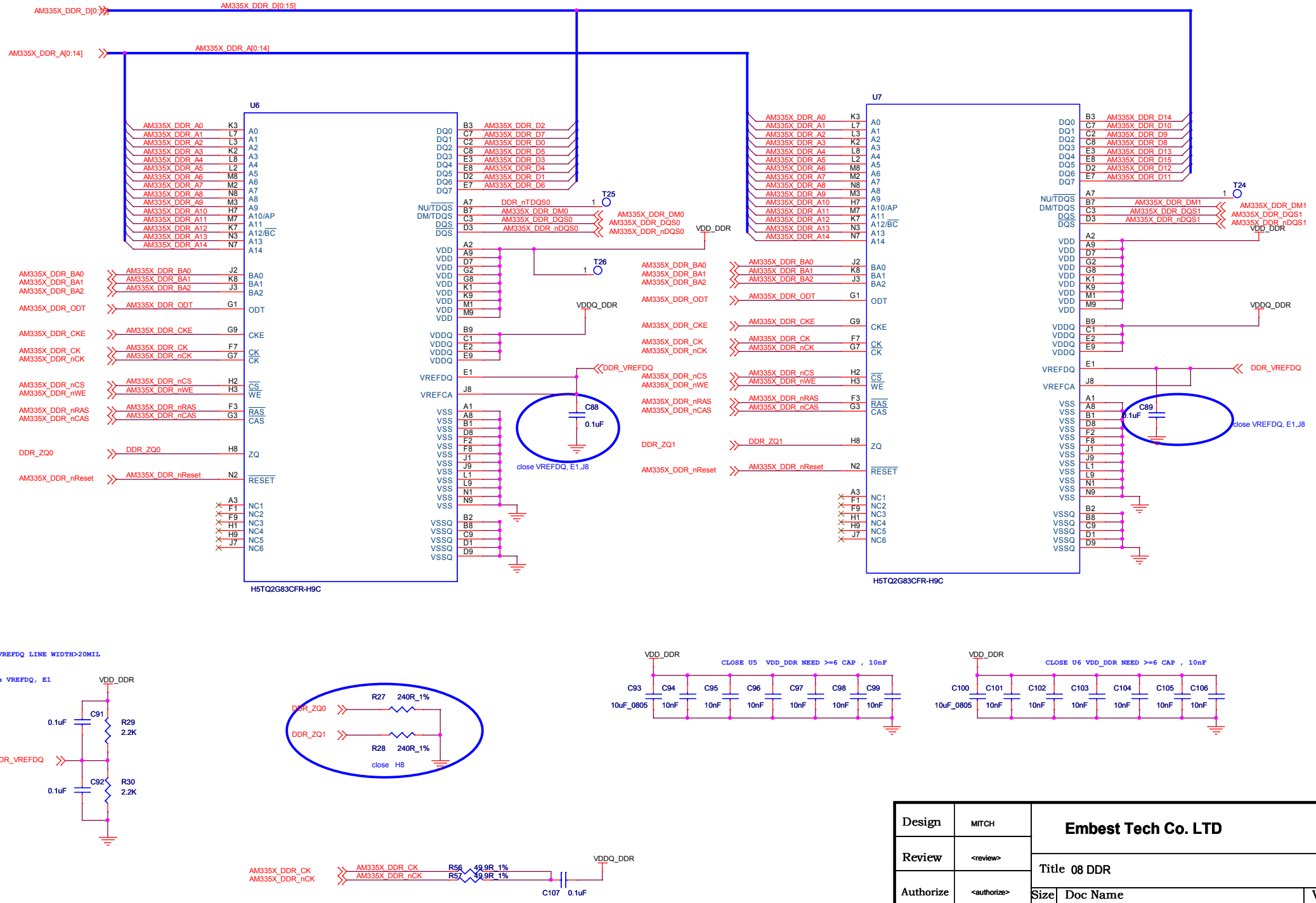
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 7 of 11



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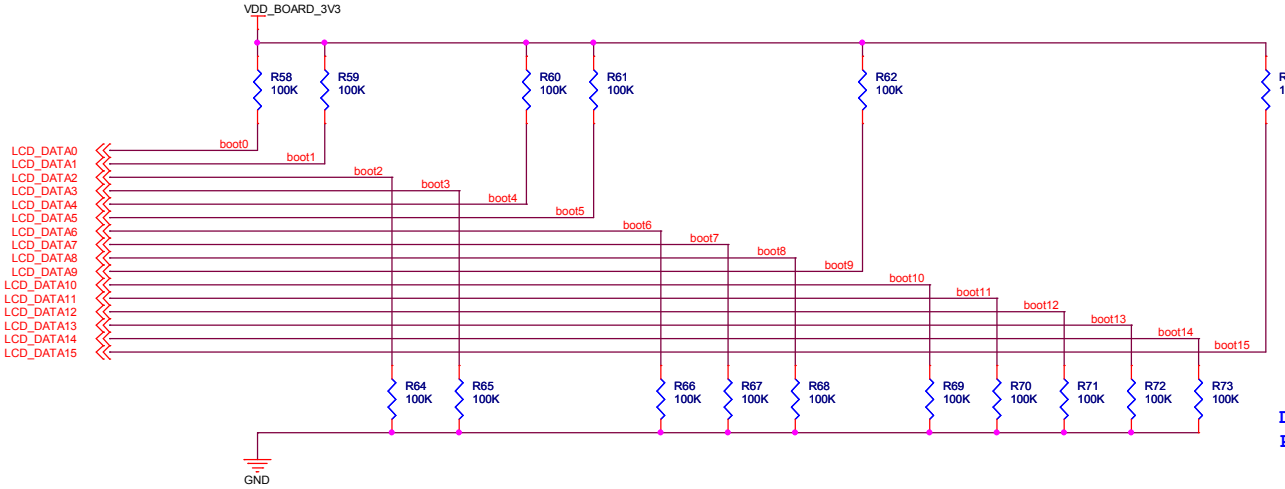
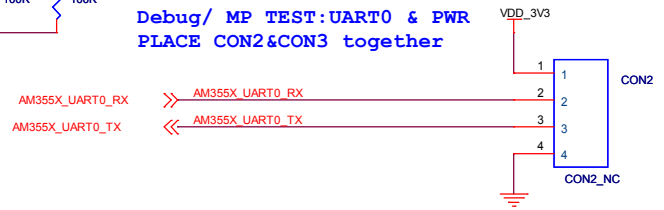
Review	<review>	Title 08 DDR	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 8 of 11

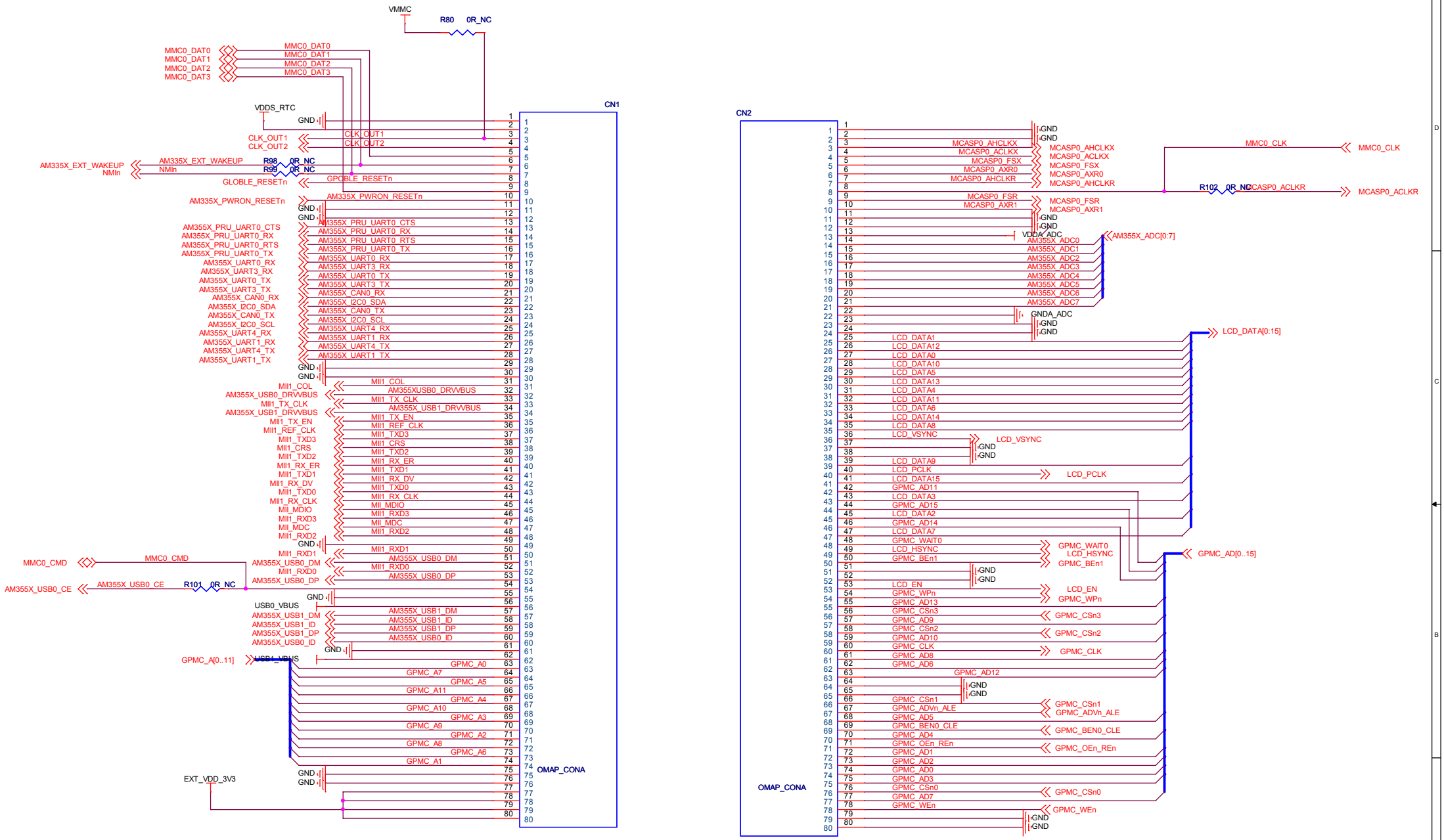
BOOT[15...0]: 1000 0010 0011 0011b
BOOT[4...0]: 10011 NAND-NANDI2C-MMC0-UART0 || 10111 MMC0->SPI0->UART0->USB0
BOOT[5]: 1b CLKOUT1=enable
BOOT[7,6]:00b MII
BOOT[8]: 0b 8BIT NAND
BOOT[9]: 1b ECC by NAND
BOOT[11,10]:00b for NAND
BOOT[13,12]: 00b by default
BOOT[15,14}: 10b 25MHz



Debug/ MP TEST:UART0 & PWR
PLACE CON2&CON3 together



Design	MITCH	**Embest Tech Co. LTD**	
Review	<review>	Title 09 MMC& EEROM	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 9 of 11



Design	MITCH	**Embest Tech Co. LTD**	
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Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 / Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 10 of 11

MINI8600B Revision History

MINI8600B 110900

1 Modify From MINI8600 0601, Add Nand;RM TF card .

Design	MITCH	**Embest Tech Co. LTD**	
Review	<review>	Title 11 REVISION HISTORY	
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900 Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 11 of 11